

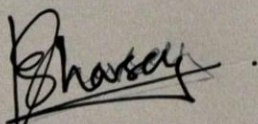
**School of Technology, Institute of Technology,
Nirma University**
Department of Electronics and Communication Engineering
B.Tech. Semester-V Division: A & B
Schedule of Quiz 1

Date: 10/8/2020

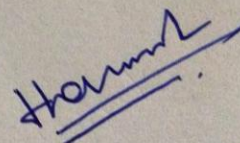
Course Name & Code	Date and Day	Time
2EC502 Digital Signal Processing	17/8/2020 Monday	11:30 am
2EC501 VLSI Design	18/8/2020 Tuesday	11:30 am
2ECDE52 Fibre Optic Communication 2ECDE54 System on Chip Design 2ECDE55 Scripting Language	19/8/2020 Wednesday	11:30 am
2EC503 Digital Communication	21/8/2020 Friday	11:30 am

Note:

1. Duration of quiz will be conveyed by respective course coordinator.
2. Maximum Marks will be 10 Marks.



Internal Exam Coordinator (EC)



HOD (EC)